



## **Prof. Jiro Hirokawa**

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**Prof. Jiro Hirokawa** received the B.S., M.S. and D.E. degrees in electrical and electronic engineering from Tokyo Institute of Technology (Tokyo Tech), Tokyo, Japan in 1988, 1990 and 1994, respectively. He was a Research Associate from 1990 to 1996 and an Associate Professor from 1996 to 2015 at Tokyo Tech. He is currently a Professor there. He was with the antenna group of Chalmers University of Technology, Gothenburg, Sweden, as a Postdoctoral Fellow from 1994 to 1995. His research area has been in slotted waveguide array antennas and millimeter-wave antennas. He received IEEE AP-S Tokyo Chapter Young Engineer Award in 1991, Young Engineer Award from IEICE in 1996, Tokyo Tech Award for Challenging Research in 2003, Young Scientists' Prize from the Minister of Education, Cultures, Sports, Science and Technology in Japan in 2005, Best Paper Award in 2007 and a Best Letter Award in 2009 from IEICE Communications Society, and IEICE Best Paper Award in 2016. He is a Fellow of IEEE and IEICE.